

12.2.1 State-transition Diagrams

Name: _____ Class: _____ Date: _____

Total: 29 marks

KEY WORDS state • transition • input • output • self-loop

Objective

Read and construct the state-transition diagrams used in the exam: convert a table into labelled arrows, handle self-loops and missing outputs, and check a diagram row by row. This sheet prepares you for past-paper set **12.2.1 State-transition Diagrams**.

1 Key knowledge and worked examples

■ Key knowledge: the exam notation

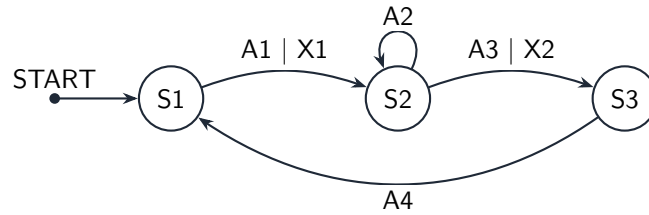
A **state-transition diagram** documents an algorithm by showing its possible states and how an input changes the current state and may produce an output.

- A circle contains a **state** name, such as S1. A filled START dot and an arrow indicate the initial state.
- A directed arrow goes from the **current state** to the **next state**. Its label is **input | output**, for example A1 | X1. The vertical bar separates input from output; it does not mean 'or'.
- If the table's Output cell is blank, label the arrow with the **input alone**. Do not invent an output or write 'None'.
- If current state equals next state, draw a **self-loop** back to the same circle. An input can produce output without changing the state.
- The same input may do different things in different states. Always identify the current state first.
- The diagram represents only the transitions given. A state with no outgoing rows needs no invented return arrow. Do not add a double circle unless the question specifies a final-state symbol.

■ Worked example: transition table to diagram

The initial state is S1. Each row specifies one arrow:

Current state	Input	Output	Next state
S1	A1	X1	S2
S2	A2		S2
S2	A3	X2	S3
S3	A4		S1



1. Draw and label all three state circles. Put START into S1.
2. Row 1 gives an arrow S1 to S2 labelled A1 | X1.
3. Row 2 has S2 as both states, so draw a self-loop labelled A2. Its output is blank.
4. Row 3 gives S2 to S3 labelled A3 | X2.
5. Row 4 gives S3 to S1 labelled A4, with no output.
6. Audit each row against one arrow: source, destination, input and output. Then check that no extra arrows were added.

■ Worked example: trace inputs and outputs

Start at S1 and apply A1, A2, A3, A4 in order.

Current state	Input	Output	Next state
S1	A1	X1	S2
S2	A2		S2
S2	A3	X2	S3
S3	A4		S1

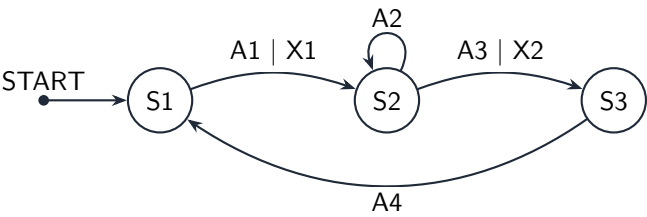
After A1 the state is S2; A2 follows the self-loop so it remains S2. A3 moves to S3 and A4 returns to S1. The output sequence is **X1, X2**; the blank cells produce no output.

■ Key knowledge: larger exam diagrams

For five-state questions, place circles to leave space for arrows and labels. Two inputs can share a source and destination: draw two labelled arrows, or clearly list both input-output pairs on one arrow. Different outputs must stay paired with their own inputs. Keep arrowheads clear and labels away from crossings. A structure chart shows module hierarchy and parameter passing; a state-transition diagram shows input-driven behaviour over time.

2 Practice

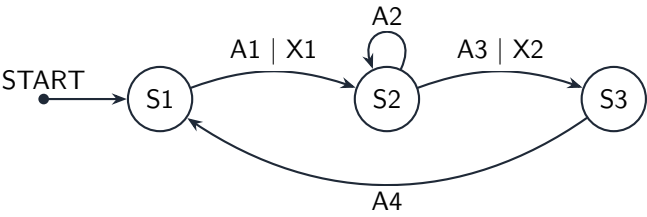
2.1 Use the diagram below. Complete the missing entries. A dash in the Output column means no output. [3]



Current state	Input	Output	Next state
S1	A1	_____	S2
S2	A2	-	_____
S2	_____	X2	S3

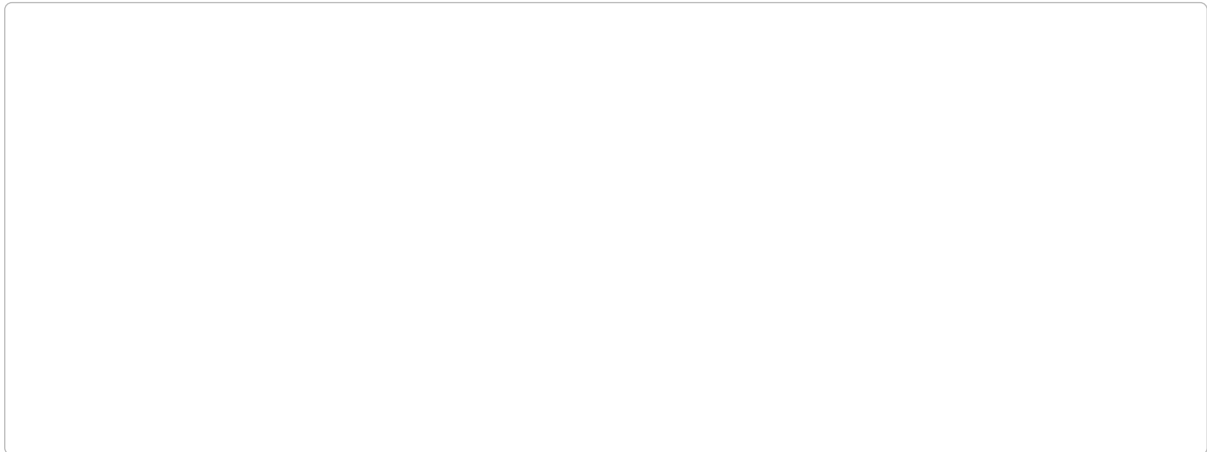
2.2 A table row is S4, A3, blank output, S4. Draw the single transition it represents. [2]

2.3 Start at S1 in the diagram below. The inputs are A1, A2, A2, A3. State the final state and the output sequence. [2]



2.4 (a) Draw a complete state-transition diagram from the table. The initial state is S1. [4]

Current state	Input	Output	Next state
S1	A1	X1	S2
S2	A2		S2
S2	A3	X2	S3
S3	A1		S1

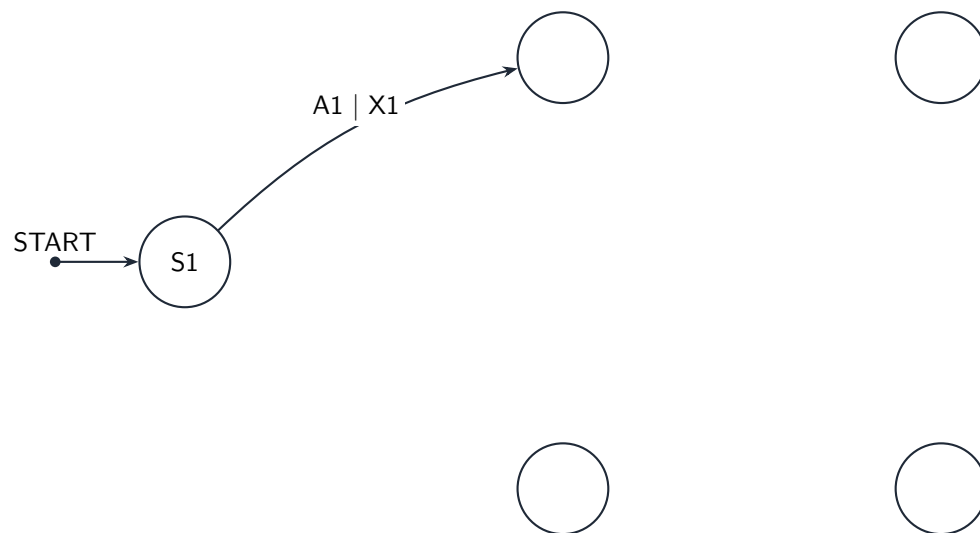


(b) Explain why the same input A1 produces different outputs in states S1 and S3. [2]

3 Exam-style questions

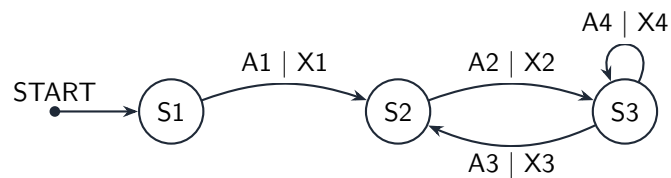
3.1 Complete the five-state diagram from the table. Its first transition and START marker are supplied. Add the missing state labels and arrows directly to the diagram.[5]

Current state	Input	Output	Next state
S1	A1	X1	S2
S1	A2		S3
S2	A3	X2	S2
S2	A4	X3	S4
S3	A1		S2
S3	A4	X3	S5
S4	A2		S3
S4	A4	X4	S5



3.2 The following diagram contains four errors relative to this table. Identify each error and state its correction. [4]

Current state	Input	Output	Next state
S1	A1	X1	S2
S2	A2		S2
S2	A3	X2	S3
S3	A4		S1



3.3 (a) Draw a complete state-transition diagram from the table. The initial state is S1. [5]

Current state	Input	Output	Next state
S1	A1		S2
S2	A2	X2	S3
S3	A1	X1	S3
S3	A2	X1	S3
S3	A3	X3	S4
S3	A4		S2
S4	A1	X4	S5
S4	A3	X4	S5
S4	A4		S2

(b) Start at S1. For inputs A1, A2, A1, A2, A3, A4, give the final state and the output sequence. [2]
